

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S2	3	"5911841".pn. or "6330635".pn. or "6421765".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/01/04 12:35
S3	1	LPC same flash same protocol\$1 same interfac\$3 same control\$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/01/31 14:51
S4	13	LPC same (rom or non-volatile) same protocol\$1 same interfac\$3 same control\$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/01/31 14:52
S5	13	(LPC or (low pin count)) same (rom or non-volatile) same protocol\$1 same interfac\$3 same control\$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/01/31 14:54
S7	5	US-5805931-\$.DID. OR US-5367646-\$.DID. OR US-6642391-\$.DID. OR US-6188602-\$.DID. OR US-6002631-\$.DID.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/01/31 15:06
S8	5	US-5805931-\$.DID. OR US-5367646-\$.DID. OR US-6542391-\$.DID. OR US-6188602-\$.DID. OR US-6002631-\$.DID.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/01/31 15:06
S9	7	LPC flash	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/01/31 15:16
S12	23	LPC adj3 flash	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/01/31 15:16
S14	4	FWH protocol	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/01 14:55
S15	26	FWH same LPC	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/01 14:55
S18	1	(LPC bus) same aLPC	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/01 20:54
S20	2	(LPC bus) same flash same protocol	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/01 20:57
S21	18	(LPC) same flash same protocol\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/01 20:57
S23	5	conver\$4 same (LPC or (low pin count)) same (FWH or (firmware hub))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/01 21:02
S25	16	(conversion or convert\$4) same (LPC or (low pin count)) same protocol	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/01 21:03

S29	412	LPC same PCI	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/01 21:04
S30	0	LPC same PCI same negotiat\$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/01 21:04
S31	18	LPC same PCI and negotiat\$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/01 21:06
S32	20	bit negotiation	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/01 21:07
S19	97	(LPC bus) same flash	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/01 21:54
S34	5	(LPC bus) same (four near3 cycle\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/01 21:54
S37	2	single near3 LPC near3 bit	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/01 21:57
S35	7	(LPC bus) same (four near3 (clock\$1 or cycle\$1))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/01 21:59
S38	7	(LPC bus) same (four near3 (clock\$1))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/01 22:01
S40	0	(single near3 (line or bit)) same (LPC or (low count pin)) same serializ\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/01 22:05
S41	39	(single near3 (line or bit)) same (LPC or (low count pin))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/01 22:05
S43	4958	(serial near3 parallel) same buffer\$3 same (convert\$3 or conversion)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/01 22:13
S44	4705	(serial near3 parallel) same (shift register) same (convert\$3 or conversion)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/01 22:13
S45	2429	(serial near3 parallel) near5 (shift register) near5 (convert\$3 or conversion)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/01 22:14
S46	0	((serial near3 parallel) near5 (shift register) near5 (convert\$3 or conversion)) same (LPC or (low pin count))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/01 22:14

S47	50	(low pin count).ti.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/01 22:14
S50	291	LPC near3 interfac\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/01 22:34
S52	3	(variable width) same (LPC or (low pin count))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 10:11
S53	3	(variable adj3 width) same (LPC or (low pin count))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 10:11
S55	3	(serial near3 parallel) same (LPC or (low pin count)) same (conversion or convert\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 10:12
S54	134	(serial near3 parallel) same (LPC or (low pin count))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 10:15
S57	84	(serial near3 parallel) same (LPC or (low pin count)) same memory	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 10:15
S59	0	((serial peripheral interface) or SPI) same ((low pin count) or LPC) same (convert\$3 or conversion)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 10:26
S62	425	flash same (conversion or convert\$3) same protocol\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 10:46
S60	14	((serial peripheral interface) or SPI) same ((low pin count) or LPC) same interfac\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 10:53
S64	8	((serial peripheral interface) or SPI) same ((low pin count) or LPC) same protocol\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 10:53
S66	64	non-volatile same LPC	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 11:02
S71	94	BIOS same (updat\$3 or upgrad\$3) same serial	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 11:50
S76	2	(single bit) near5 (LPC or (low pin count))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 14:08
S77	0	(serial to parallel) same LPC	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 14:10

S78	1255	convert\$3 same LPC	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 14:11
S79	0	convert\$3 same (single bit) same LPC	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 14:11
S80	0	convert\$3 same SPI same LPC	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 14:12
S81	33	LPC protocol	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 14:31
S84	62	LPC near3 protocol	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 14:31
S86	1136	serial peripheral interface	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 14:34
S87	152	(serial peripheral interface) same (parallel or LPC or (low pin count))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 14:35
S89	1580	protocol converter	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 14:42
S90	124	(protocol converter).ti.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 14:42
S92	75	single near3 LPC	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 14:47
S94	3642	first same second same protocol\$1 same (conversion or convert\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 15:08
S96	781	((first same second) adj3 protocol\$1) same (conversion or convert\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 15:10
S98	9	((first same second) adj3 protocol\$1) same (conversion or convert\$3) same (serial near3 parallel)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 15:15
S99	134	((low pin count) or LPC) same (serial near3 parallel)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 15:15
S10 1	2	LPC same (variable near3 width)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 20:15

S10 3	45	(low pin count) near5 speed	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 20:18
S10 5	22	(single near3 pin) same (low pin count)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 20:26
S10 7	3409	LAD	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 20:29
S10 8	23	LAD same (LPC or (low pint count))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 20:29
S11 0	7062	SPI	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 20:47
S11 1	2	Intel.as. and (FWH same programming)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 20:48
S11 2	8	(FWH same programming)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 20:54
S11 3	52	(non-volatile memory) same (LPC or (low pin\$1 count))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 20:55
S10 2	1	(low pin count) same (variable near3 width)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 20:59
S11 5	43	(low pin count) same (conversion or convert\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 20:59
S11 7	33	LPC protocol	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 21:05
S11 9	1154	super i/o	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 21:07
S12 0	123	(super i/o) same LPC	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 21:07
S12 4	23	modified LPC	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 21:40
S12 6	177	(silicon storage).as.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 21:43

S12 7	11443	protocol near3 (conversion or convert\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 21:53
S12 8	2	S127 same LPC	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 21:53
S12 9	0	S127 same (low pin couint)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 21:53
S12 2	6369	protocol (conversion or convert\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 21:54
S13 0	75	(non-volatile or flash) same (protocol (conversion or convert\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 21:54
S13 2	1230	serdes	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 21:55
S13 5	1	(single near3 (pin or line)) same (LPC (protocol or interface))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 22:08
S13 6	0	(conversion or convert\$3) near5 (FWH and LPC)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 22:09
S13 7	0	(conversion or convert\$3) near5 (FWH same LPC)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 22:09
S13 8	7	("5805882" "5968141" "6032221" "6202123" "6266810" "6343617").PN. OR ("6718407").URPN.	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/02/02 22:20
S12 3	2	S122 same LPC	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 22:31
S13 9	195	S122 same match\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 22:31
S14 1	8065	(non-volatile adj3 memory) same programming	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 22:37
S14 2	228	(non-volatile adj3 memory) same programming same protocol	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	ADJ	ON	2006/02/02 22:37
S1	6	"5911841".pn. or "6330635".pn. or "6421765".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/02/03 09:39

L1	78	(((multiple or plural) port) or multi-port) same protocol\$1 same (conversion or convert\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/02/03 09:40
L4	890	710/104\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/02/03 09:50
L5	619	710/105\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/02/03 09:50
L6	1664	710/305\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/02/03 09:50
L7	1495	713/2\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/02/03 09:50
L8	1042	713/100\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/02/03 09:50

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	8	LPC same (rom or non-volatile) same protocol\$1 same interfac\$3 same control\$4	US-PGPUB	ADJ	ON	2006/02/03 15:31
L2	17	LPC adj3 flash	US-PGPUB	ADJ	ON	2006/02/03 15:31
L4	1294	710/105\$.ccls. or 710/305\$.ccls. or 711/103\$.ccls. or 713/100\$.ccls.	US-PGPUB	ADJ	ON	2006/02/03 15:34
L6	175	L4 and (BIOS or LPC or FWH)	US-PGPUB	ADJ	ON	2006/02/03 15:34
L7	39	(non-volatile or flash) same (protocol (conversion or convert\$3))	US-PGPUB	ADJ	ON	2006/02/03 15:35